

REMARKS

Applicant thanks the Examiner for the thorough consideration given the present application. Claims 1-26 are currently being prosecuted. The Examiner is respectfully requested to reconsider his rejections in view of the amendments and remarks as set forth below.

Allowable Subject Matter

It is gratefully acknowledged that the Examiner considers the subject matter of claims 1-8 as being allowable.

Rejection under 35 U.S.C. § 103

Claims 9, 10, 15-19, 22, 23, 25, and 26 stand rejected under 35 U.S.C. § 103 as being obvious over Katakura et al. (USP 5,754,154) in view of Kikuo et al. (USP 5,250,937) and Lee (USP 5,940,055). This rejection is respectfully traversed.

The Examiner relies on the Katakura et al. reference to show a charge characteristic compensating circuit having a voltage supply for generating a gate voltage, a gate line driver and a current controller. The Examiner admits that Katakura et al. fails to teach:

1) gate lines and a gate line driver connected to the scan line and scan line driver;

- 2) a voltage converter generating a high level gate voltage;
- 3) a gate line controller including a resistor and thermistor, receiving the high level gate voltage from the voltage converter and supplying a controlling signal that varies as an ambient temperature; and
- 4) a gate line driver receiving the controlling signal from the gate line controller and driving a gate line.

The Examiner relies on Kikuo et al. to teach: 1) gate lines and a gate line driver connected to a scan line and a scan line driver; 2) a voltage converter generating a high level gate voltage; 3) a gate line controller including resistor and thermistor receiving the high level gate voltage from the voltage converter and supplying a controlling signal that varies as an ambient temperature varies; and 4) a gate line driver receiving the controlling signal from the gate line driver and driving a gate line. The Examiner admits that the combination of these two references fails to teach specifically supplying a predetermined voltage to a gate line according to a controlling signal to drive the gate line. The Examiner relies on the Lee reference to show supplying a predetermined voltage. By way of the present Amendment, Applicant has amended independent claims 9 and 22 to make it clear that the voltage supplied to the gate line is a voltage that is varied according to the controlling signal to drive the gate line. Applicant submits that the Lee reference does not show voltage that is varied in accordance with the control signal. Instead, Lee discloses that

the gate driver applies gate driving voltages to the gate lines to control the transistors connected thereto by external control signals supplied to the gate driver. Accordingly, Applicant submits that the combination of these three references does not teach the invention as described in claims 9 and 22.

Further, Applicant submits that it would not be obvious to combine these three different references. The Examiner has not provided proper motivation for one skilled in the art to teach that the various features of these three references should be combined. Accordingly, Applicant submits that independent claims 9 and 22 are allowable.

Claims 11-14 and 24 stand rejected under 35 U.S.C. § 103 as being obvious over Katakura et al. in view of Kikuo et al., and Lee as applied above and further in view of Marks et al. (USP 5,119,215). Claims 20 and 21 stand rejected under 35 U.S.C. § 103 as being obvious over Katakura et al. in view of Kikuo et al. and Lee as applied above, and further in view of Noma et al. (USP 6,184,631). These rejections are respectfully traversed.

The Examiner cites Marks et al. to teach a positive temperature coefficient thermistor. The Examiner cites Noma et al. to teach a voltage divider including a positive temperature coefficient thermistor. However, Applicant submits that even if these two references teach these concepts, they do not aid the other three references in overcoming their deficiencies as noted above.

Accordingly, Applicant submits that claims 9-26 are allowable over any combination of these references.

Claims 10-21 and 23-26 depend from allowable claims 9 and 22 and as such, are also considered to be allowable. Furthermore, these claims each recite additional features of the invention and accordingly are considered to be additionally allowable.

Conclusion

In view of the above Remarks, it is believed that the claims clearly distinguish over the patents relied on by the Examiner, either alone or in combination. In view of this, reconsideration of the rejections and allowance of all the claims are respectfully requested.

Should there be any outstanding matters that need to be resolved in the present application, the Examiner is respectfully requested to contact Robert F. Gnuse (Reg. No. 27,295) at the telephone number of the undersigned below, to conduct an interview in an effort to expedite prosecution in connection with the present application.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fee required under 37 C.F.R. §§ 1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By: 
Joseph A. Kolasch
Reg. No. 22,463

JAK/RFG/gf
(703) 205-8000
2658-0251P

P.O. Box 747
Falls Church, Virginia 22040-0707